

The diagram illustrates a computer system 101. A central processing unit 103 is connected to a parallel bus I/F 203, a data C/V 206, a CPU peripheral C/T 207, a bus C/T 202, a compressor/extender 208, a memory access C/T 209, and a memory module 102. The CPU peripheral C/T 207 is further connected to a process C/T 104, RAM 105, and ROM 106. The memory module 102 is connected to an I/O C/T 108. The I/O C/T 108 is connected to an operation panel 111, an SBU 112, an LDD 113, a peripheral machine 114, a scanner drive mech 115, and a printer drive mech 116. The system is also connected to a network 120.

FIG. 8B

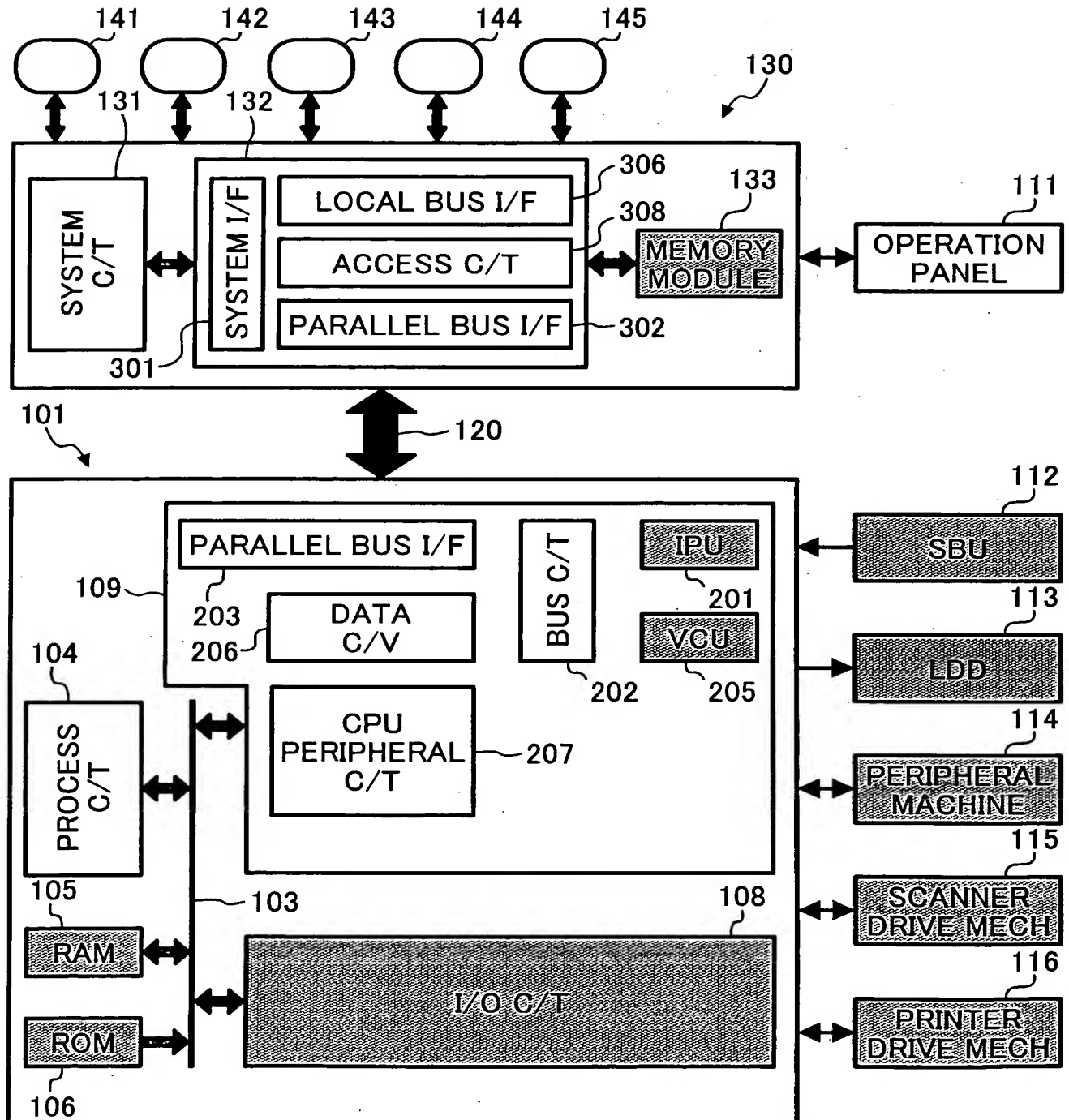
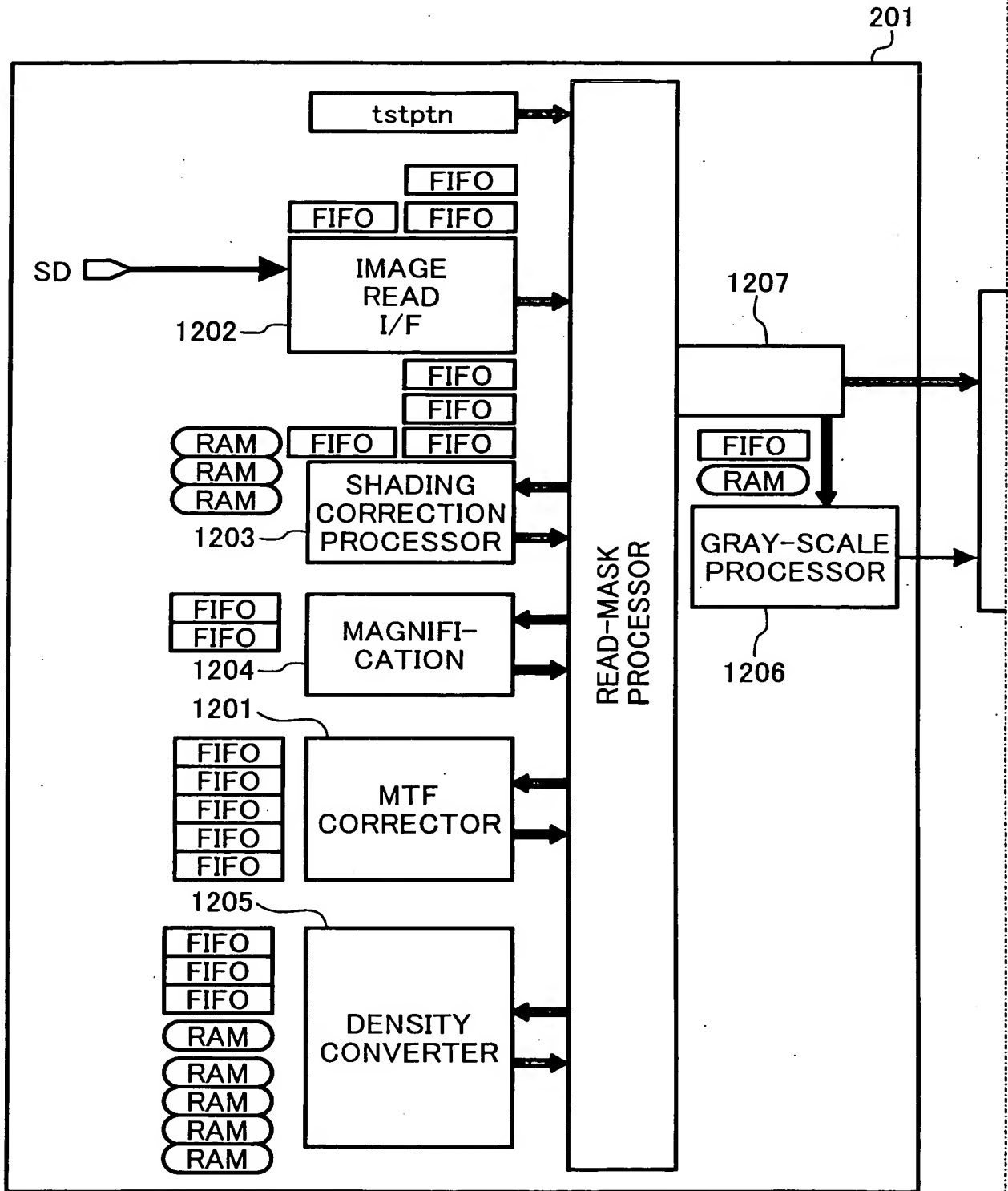


FIG. 12A

FIG. 12  
FIG. 12A  
FIG. 12B



**FIG. 13A**

FIG. 13

**FIG. 13A**

**FIG. 13B**

